

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki et al.                      Art Unit : 2811  
 Serial No. : 09/814,255                                      Examiner : Ori Nadav  
 Filed : March 21, 2001                                      Conf. No. : 5578  
 Title : SEMICONDUCTOR DEVICE AND ITS MANUFACTURING METHOD

**MAIL STOP AMENDMENT**

Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants request consideration of the references listed on the attached PTO-1449 form. Under 37 C.F.R. § 1.98 (a)(2)(ii), only copies of foreign patent documents and/or non-patent literature are enclosed. Copies of any listed U.S. patents or U.S. patent application publications can be provided upon request.

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AL	Japan 04273162 – English Abstract: <a href="mailto:esp@cenet.com">esp@cenet.com</a> .
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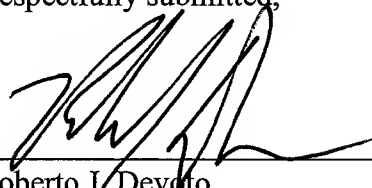
This statement is being filed after a first Office action on the merits, but before receipt of a final Office action or a Notice of Allowance. I, the undersigned, hereby certify that each item of information contained in this statement was cited in a communication from a foreign patent office in a counterpart foreign application, JP Application 08-354803, the communication being dated June 12, 2007, which is not more than three months prior to the filing of this statement. Please apply any charges or credits to Deposit Account No. 06-1050.

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Attorney's Docket No.: 07977-  
107002 / US3194/3205/3215D1

Respectfully submitted,

Date: 7/13/07

  
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